

Dual non-retriggerable monostable multivibrator with reset

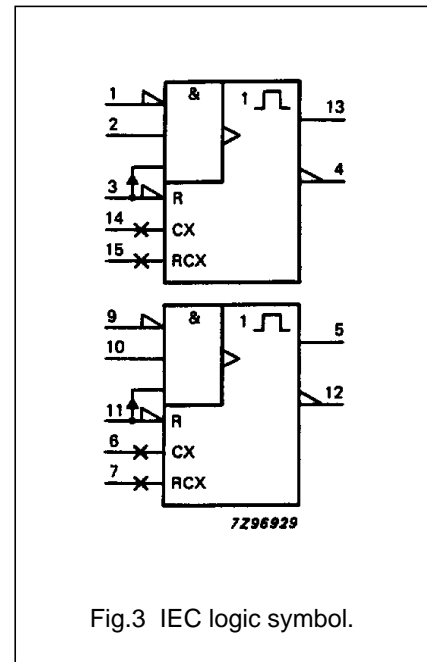
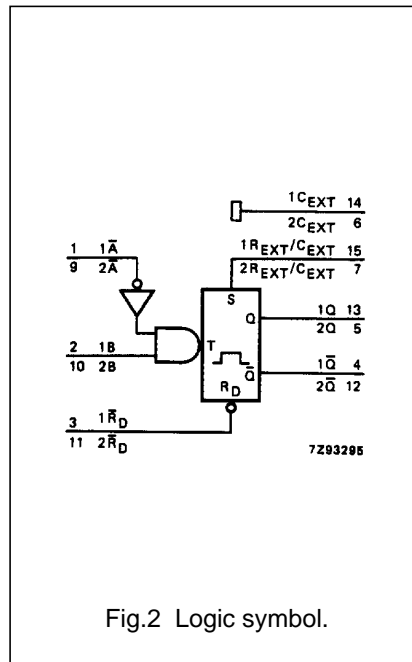
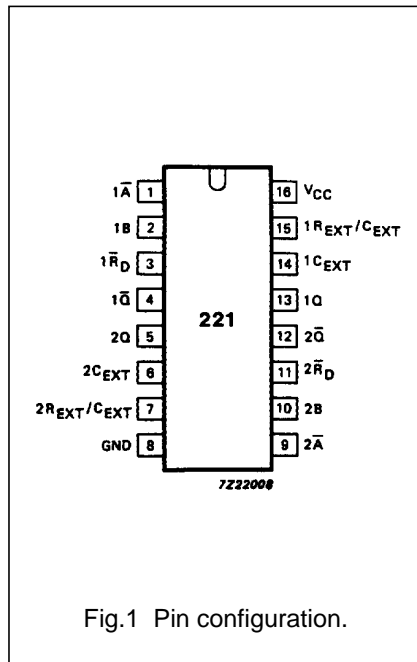
74HC/HCT221

ORDERING INFORMATION

See "74HC/HCT/HCU/HCMOS Logic Package Information".

PIN DESCRIPTION

PIN NO.	SYMBOL	NAME AND FUNCTION
1, 9	$1\bar{A}, 2\bar{A}$	trigger inputs (negative-edge triggered)
2, 10	1B, 2B	trigger inputs (positive-edge triggered)
3, 11	$1\bar{R}_D, 2\bar{R}_D$	direct reset inputs (active LOW)
4, 12	$1\bar{Q}, 2\bar{Q}$	outputs (active LOW)
7	$2R_{EXT}/C_{EXT}$	external resistor/capacitor connection
8	GND	ground (0 V)
13, 5	1Q, 2Q	outputs (active HIGH)
14, 6	$1C_{EXT}, 2C_{EXT}$	external capacitor connection
15	$1R_{EXT}/C_{EXT}$	external resistor/capacitor connection
16	V _{CC}	positive supply voltage



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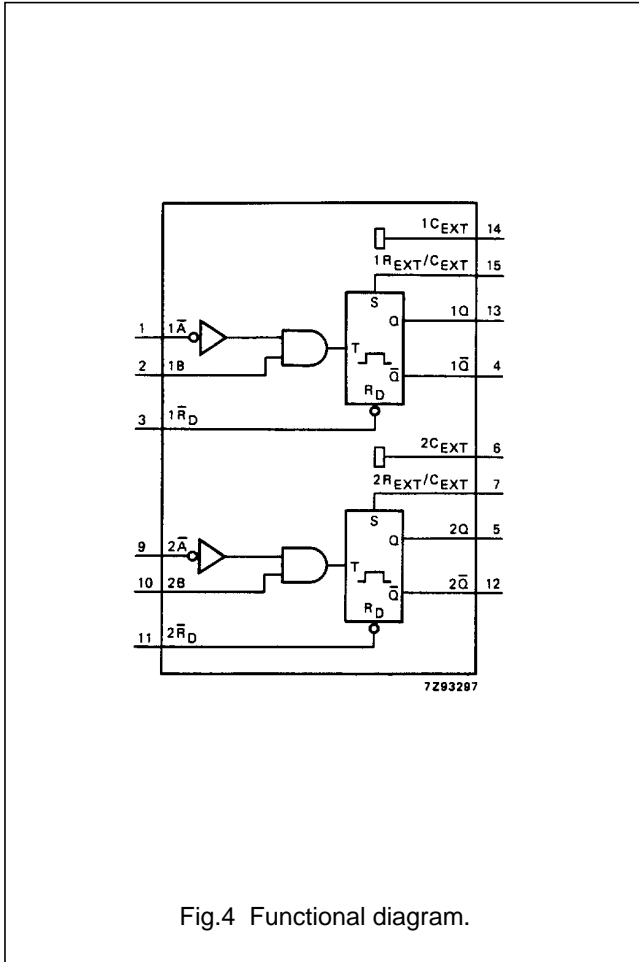


Fig.4 Functional diagram.

FUNCTION TABLE

INPUTS			OUTPUTS	
$n\bar{R}_D$	$n\bar{A}$	nB	nQ	$n\bar{Q}$
L	X	X	L	H
X	H	X	L (2)	H (2)
X	X	L	L (2)	H (2)
H	L	↑		
H	↓	H		
↑	L	H		

Notes

- H = HIGH voltage level
L = LOW voltage level
X = don't care
↑ = LOW-to-HIGH level
↓ = HIGH-to-LOW level
 = one HIGH-level output pulse
 = one LOW-level output pulse
- If the monostable was triggered before this condition was established the pulse will continue as programmed.
- For this combination the reset input must be LOW and the following sequence must be used:
pin 1 (or 9) must be set HIGH or pin 2 (or 10) set LOW;
then pin 1 (or 9) must be LOW and pin 2 (or 10) set HIGH. Now the reset input goes from LOW-to-HIGH and the device will be triggered.

