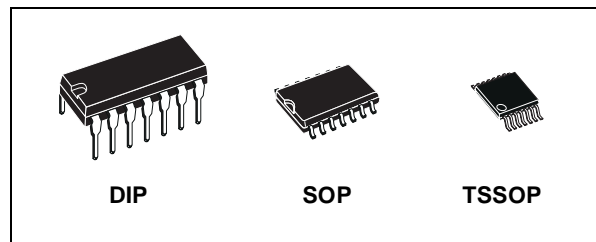




# M74HC10

## TRIPLE 3-INPUT NAND GATE

- HIGH SPEED:  
 $t_{PD} = 8\text{ns}$  (TYP.) at  $V_{CC} = 6\text{V}$
- LOW POWER DISSIPATION:  
 $I_{CC} = 1\mu\text{A}$ (MAX.) at  $T_A=25^\circ\text{C}$
- HIGH NOISE IMMUNITY:  
 $V_{NIH} = V_{NIL} = 28\% V_{CC}$  (MIN.)
- SYMMETRICAL OUTPUT IMPEDANCE:  
 $|I_{OH}| = I_{OL} = 4\text{mA}$  (MIN)
- BALANCED PROPAGATION DELAYS:  
 $t_{PLH} \cong t_{PHL}$
- WIDE OPERATING VOLTAGE RANGE:  
 $V_{CC}$  (OPR) = 2V to 6V
- PIN AND FUNCTION COMPATIBLE WITH  
 74 SERIES 10



### ORDER CODES

PACKAGE	TUBE	T & R
DIP	M74HC10B1R	
SOP	M74HC10M1R	M74HC10RM13TR
TSSOP		M74HC10TTR

### DESCRIPTION

The M74HC10 is an high speed CMOS TRIPLE 3-INPUT NAND GATE fabricated with silicon gate C<sup>2</sup>MOS technology.

The internal circuit is composed of 3 stages including buffer output, which enables high noise immunity and stable output.

All inputs are equipped with protection circuits against static discharge and transient excess voltage.

### PIN CONNECTION AND IEC LOGIC SYMBOLS

