

**SN5405, SN54LS05, SN54S05,
SN7405, SN74LS05, SN74S05**
HEX INVERTERS WITH OPEN-COLLECTOR OUTPUTS

DECEMBER 1983 — REVISED MARCH 1988

- Package Option Includes Plastic "Small Outline" Packages, Ceramic Chip Carriers and Flat Packages, and Plastic and Ceramic DIPs
- Dependable Texas Instruments Quality and Reliability

description

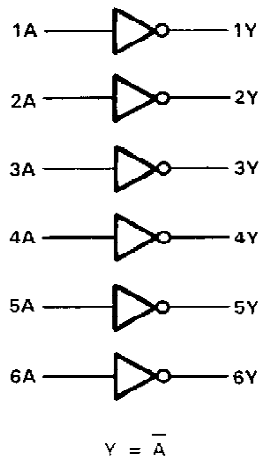
These devices contain six independent inverters. The open-collector outputs require pull-up resistors to perform correctly. They may be connected to other open-collector outputs to implement active-low wired-OR or active-high wired-AND functions. Open-collector devices are often used to generate high V_{OH} levels.

The SN5405, SN54LS05, and SN54S05 are characterized for operation over the full military temperature range of -55°C to 125°C . The SN7405, SN74LS05, and SN74S05 are characterized for operation from 0°C to 70°C .

FUNCTION TABLE (each inverter)

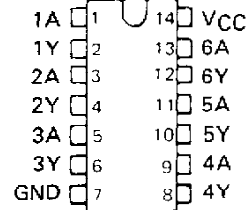
INPUT	OUTPUT
A	Y
H	L
L	H

logic diagram (positive logic)

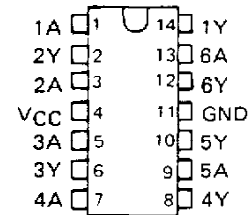


SN5405 . . . J PACKAGE
SN54LS05, SN54S05 . . . J OR W PACKAGE
SN7405 . . . N PACKAGE
SN74LS05, SN74S05 . . . D OR N PACKAGE

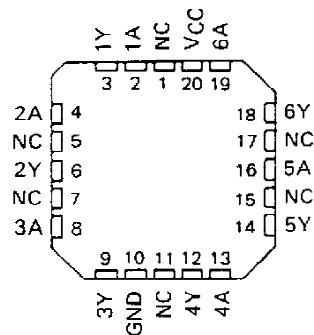
(TOP VIEW)



SN5405 . . . W PACKAGE
(TOP VIEW)

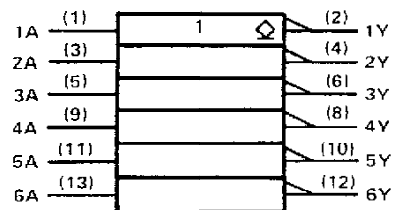


SN54LS05, SN54S05 . . . FK PACKAGE
(TOP VIEW)



NC — No internal connection

logic symbol†



† This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12. Pin numbers shown are for D, J, N, and W packages.

PRODUCTION DATA documents contain information current as of publication date. Products conform to specifications per the terms of Texas Instruments standard warranty. Production processing does not necessarily include testing of all parameters.

